

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Lancaster

Serial No.: N/A

Filed: Herewith

Title: Semiconductor Non-Volatile Memory
Device Having Improved Write Speed

Attorney Docket No.: NVX-0015C1

Group Art Unit (of Parent Application): 2822

Examiner: N/A

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Box Patent Application
Washington, D.C. 20231

Dear Sir:

Entry of the following amendments is respectfully requested.

In the Specification

Page 1, after the title, please insert -- This application is a divisional of patent application

Serial No. 09/082,167 filed May 20, 1998 -- new U.S. Patent 6,140,476

Page 19, line 10, please delete [deletion] and substitute therefor -- depletion --.

Page 22, line 19, please delete [deletion] and substitute therefor -- depletion --.

37 C.F.R. § 1.10

I hereby certify that this is being deposited with the U.S. Postal Service "Express Mail Post Office to Addressee" service under 37 CFR § 1.10 on the date indicated below and is addressed to:

Assistant Commissioner for Patents
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Washington, D.C. 20231

By: Bradley Sako

Typed Name: Bradley Sako

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Date of Deposit: OCTOBER 12, 2000